

Fig. 1

WO 2005/034418 PCT/SE2004/001438

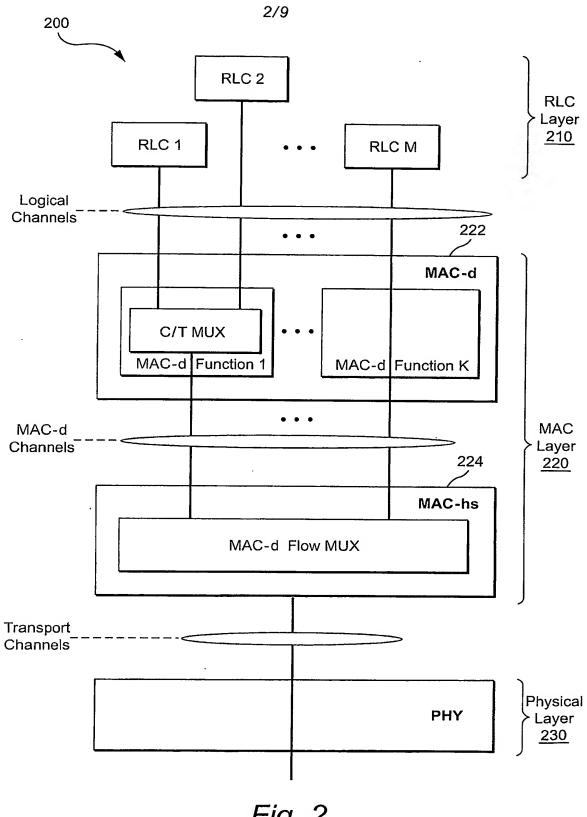


Fig. 2

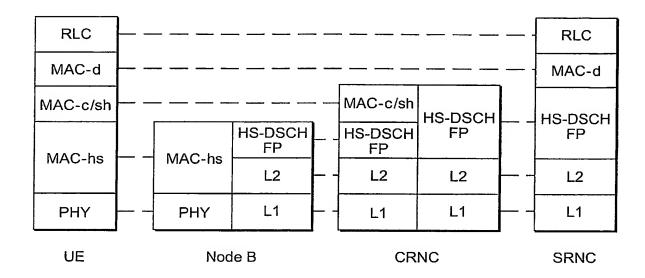


Fig. 3

WQ, 2005/034418 PCT/SE2004/001438

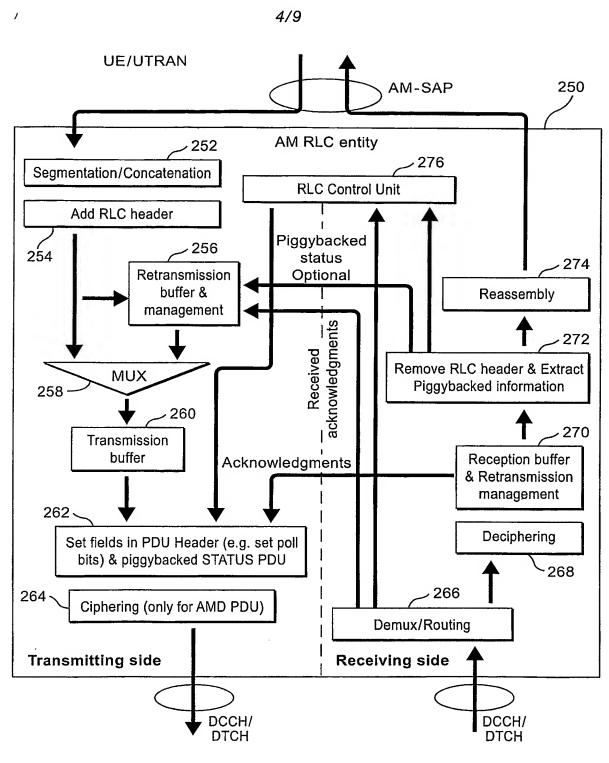
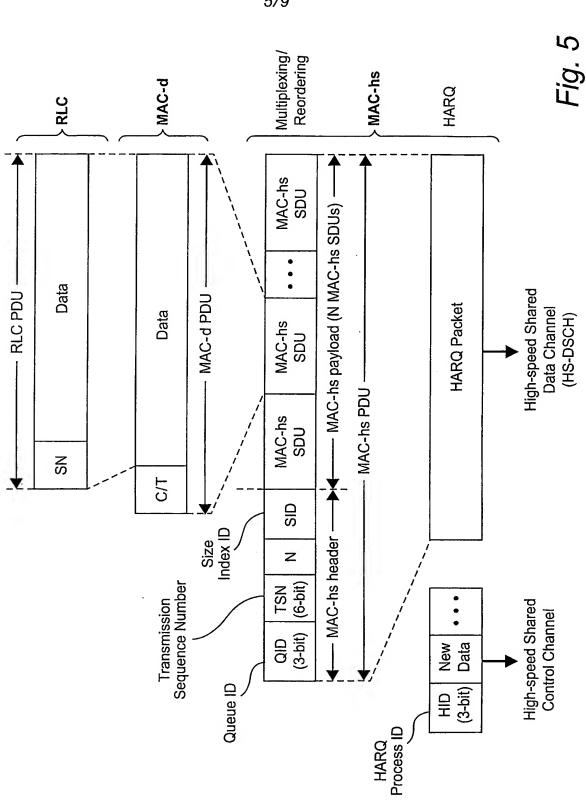


Fig. 4



5/9

6/9

## **AM RLC PDU Header**

## AMD (Data) PDU

| D/C Sequence Number           |     | Oct1               |
|-------------------------------|-----|--------------------|
| Sequence Number P             | HE  | Oct2               |
| Length Indicator              | E   | Oct3 (Optional) (1 |
| •                             |     | -                  |
| •                             |     |                    |
|                               |     | 1                  |
| Length Indicator              | E   |                    |
| Data                          |     |                    |
| PAD or a piggybacked STATUS F | טסי | OctN               |

Fig. 6

## Control PDU

|                                | _    |
|--------------------------------|------|
| D/C PDU type SUFI <sub>1</sub> | Oct1 |
| SUFI <sub>1</sub>              | Oct2 |
| • • •                          | 7    |
| SUFI <sub>K</sub>              |      |
| PAD                            | OctN |

Fig. 7

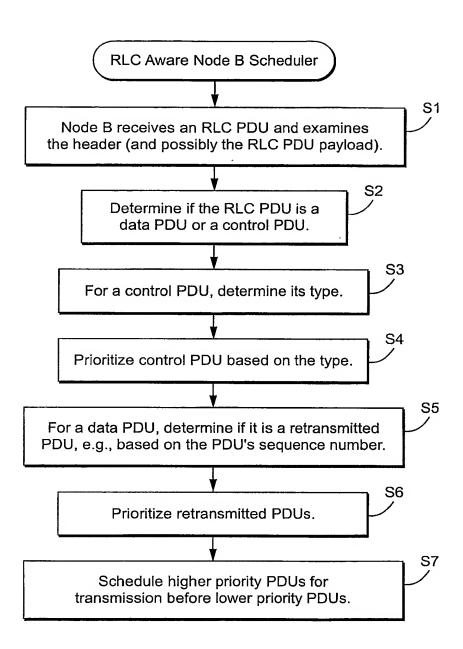


Fig. 8

